

FIG. 1

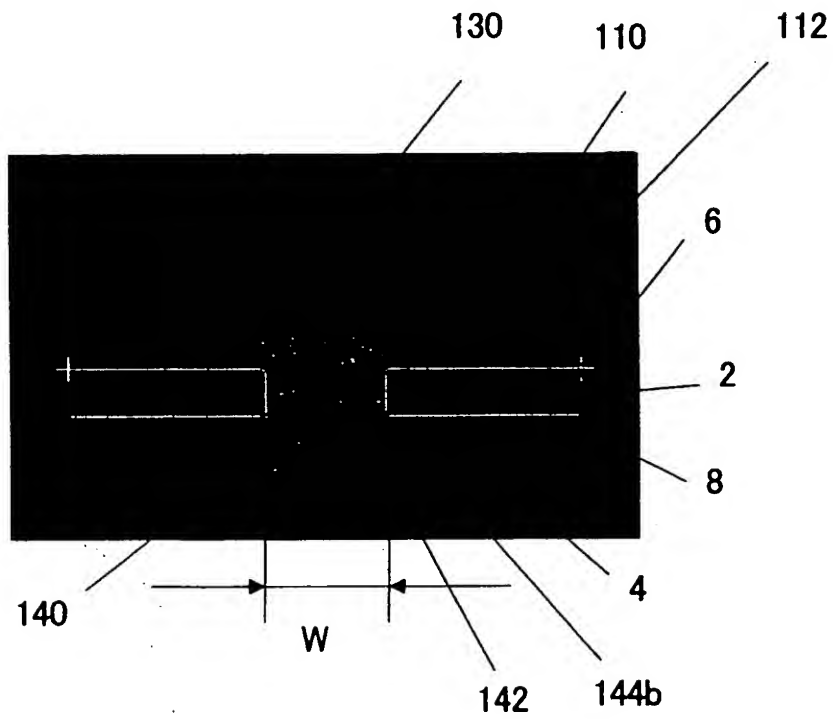


FIG. 2

—— F1RF2\_90001

—— F2RF2\_90002

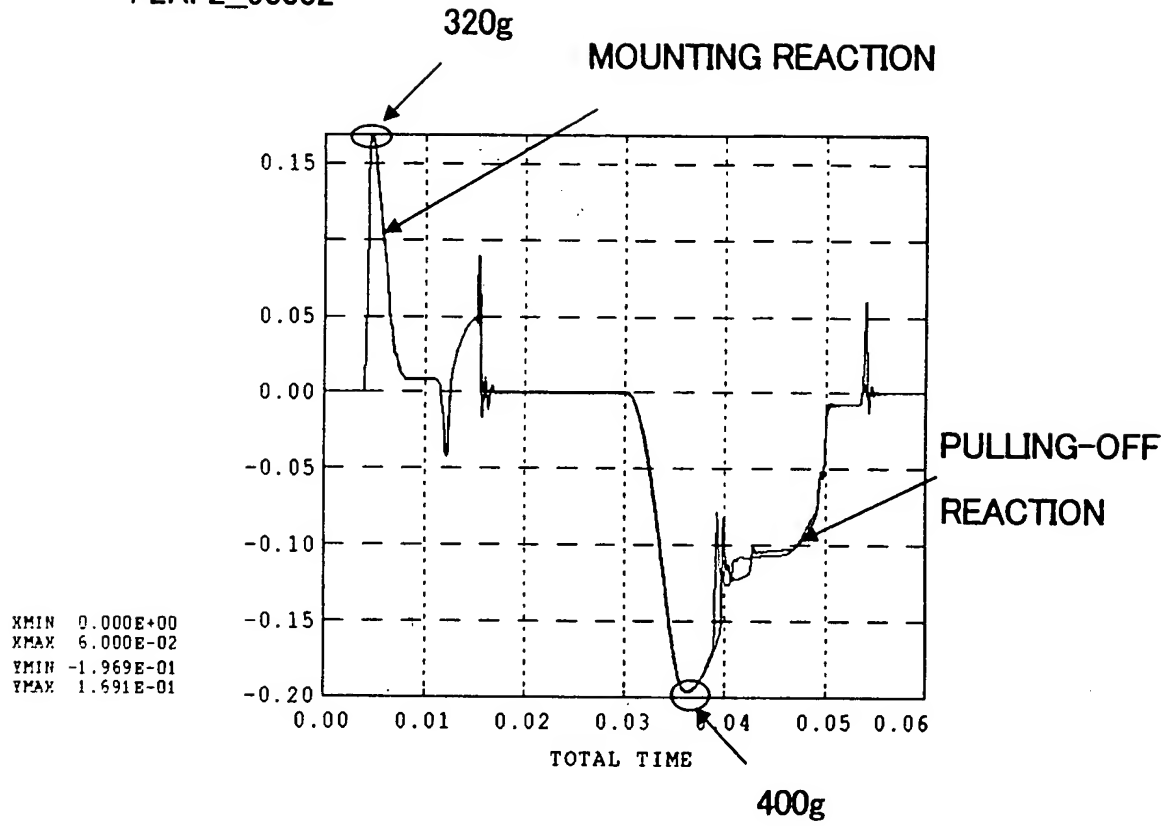


FIG. 3

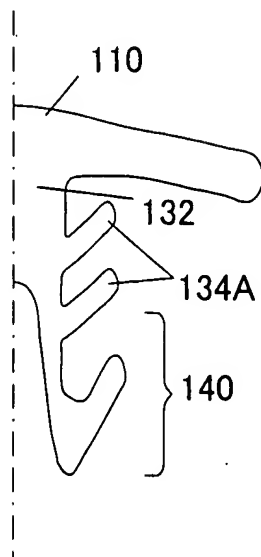


FIG. 4

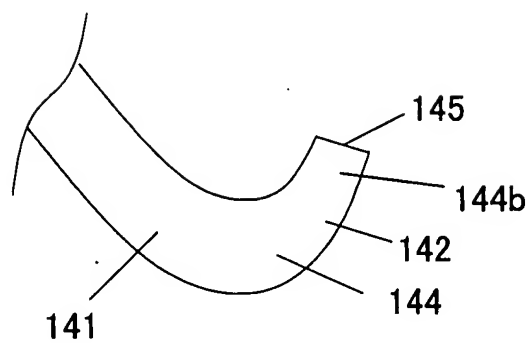


FIG. 5

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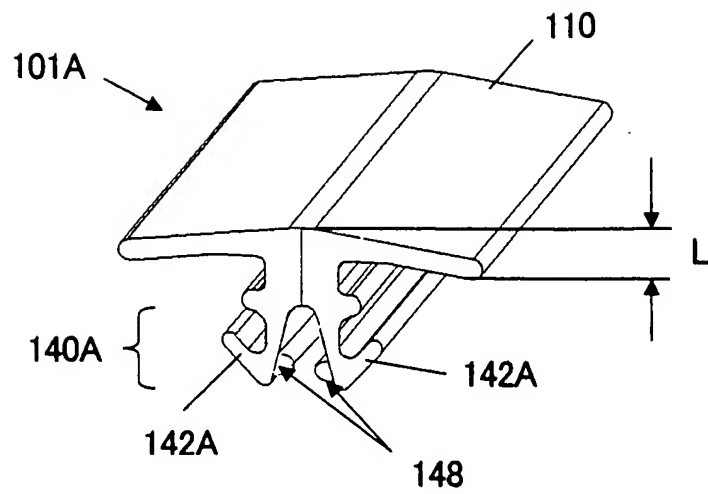


FIG. 6

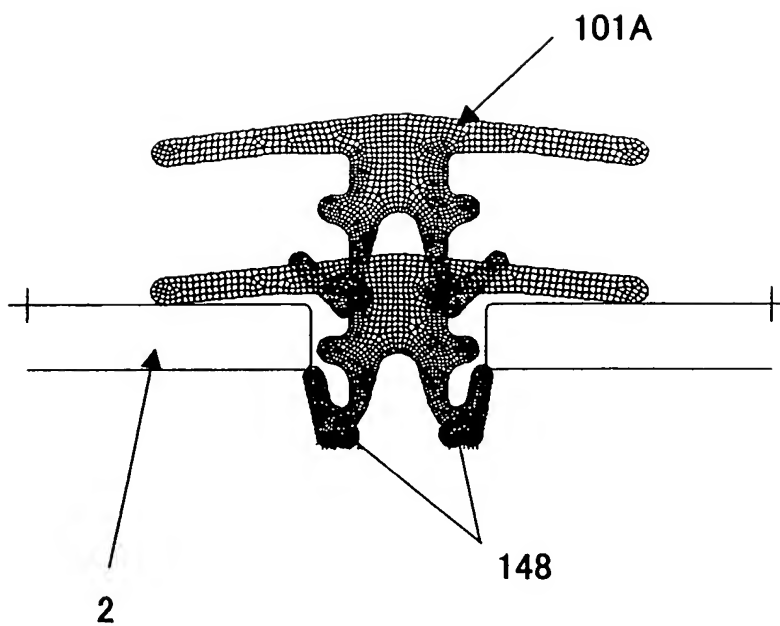


FIG. 7A

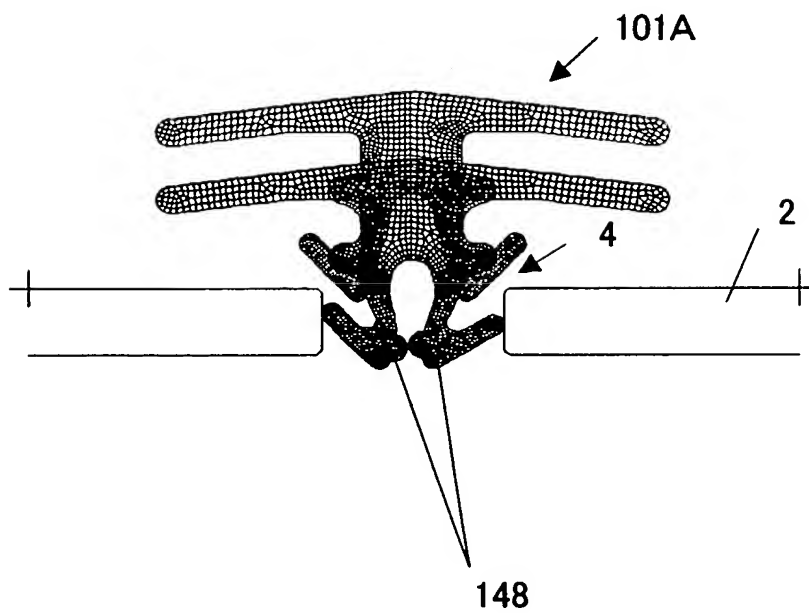


FIG. 7B

FIG. 8A

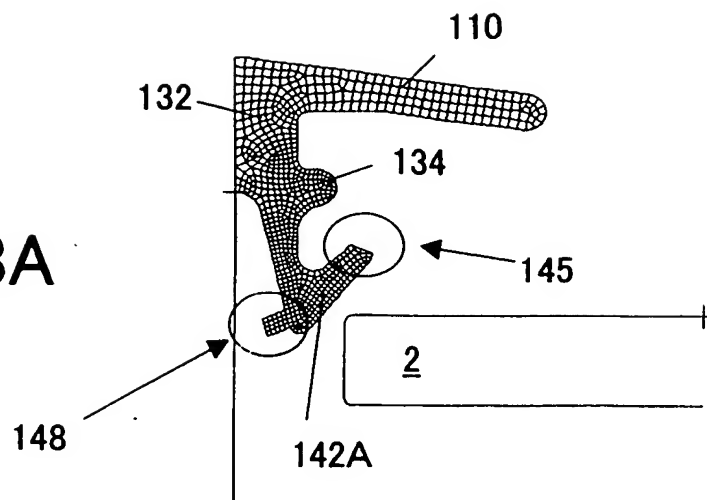


FIG. 8B

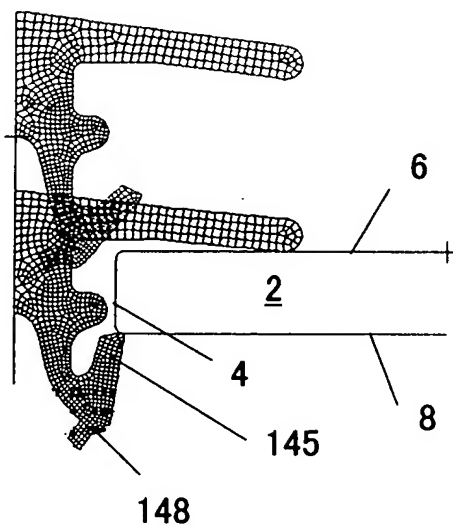
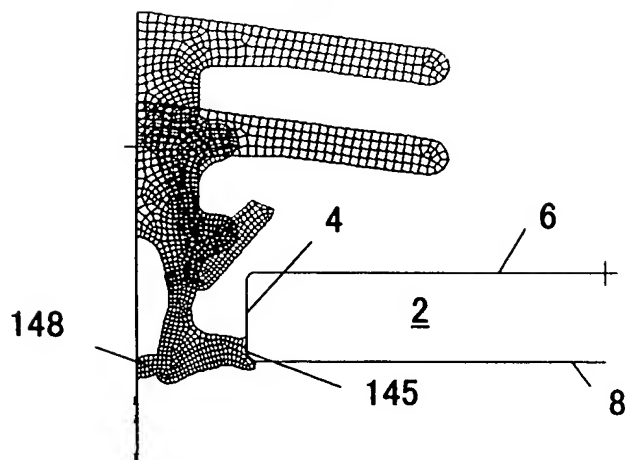
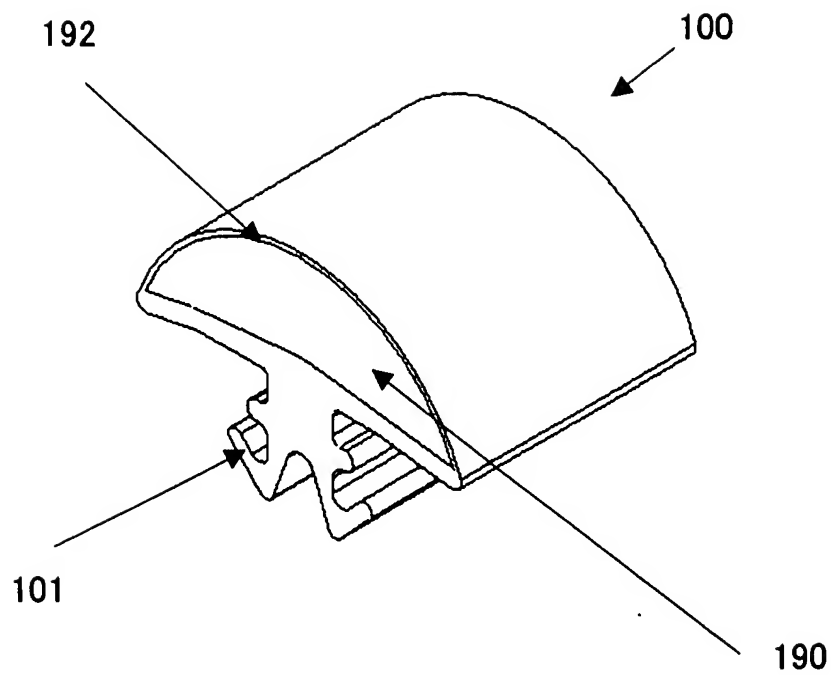


FIG. 8C





**FIG. 9**

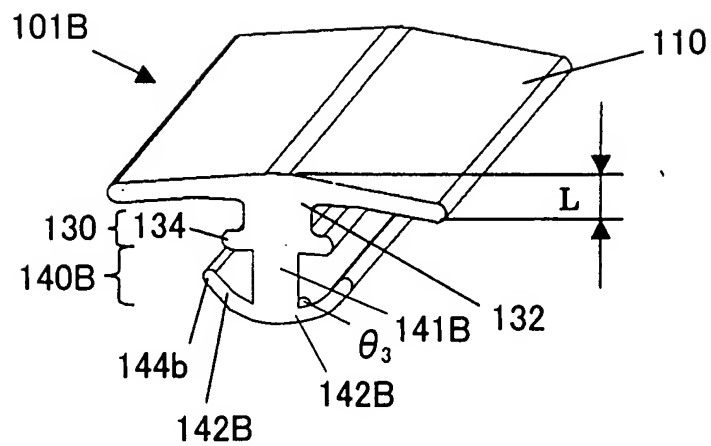


FIG. 10



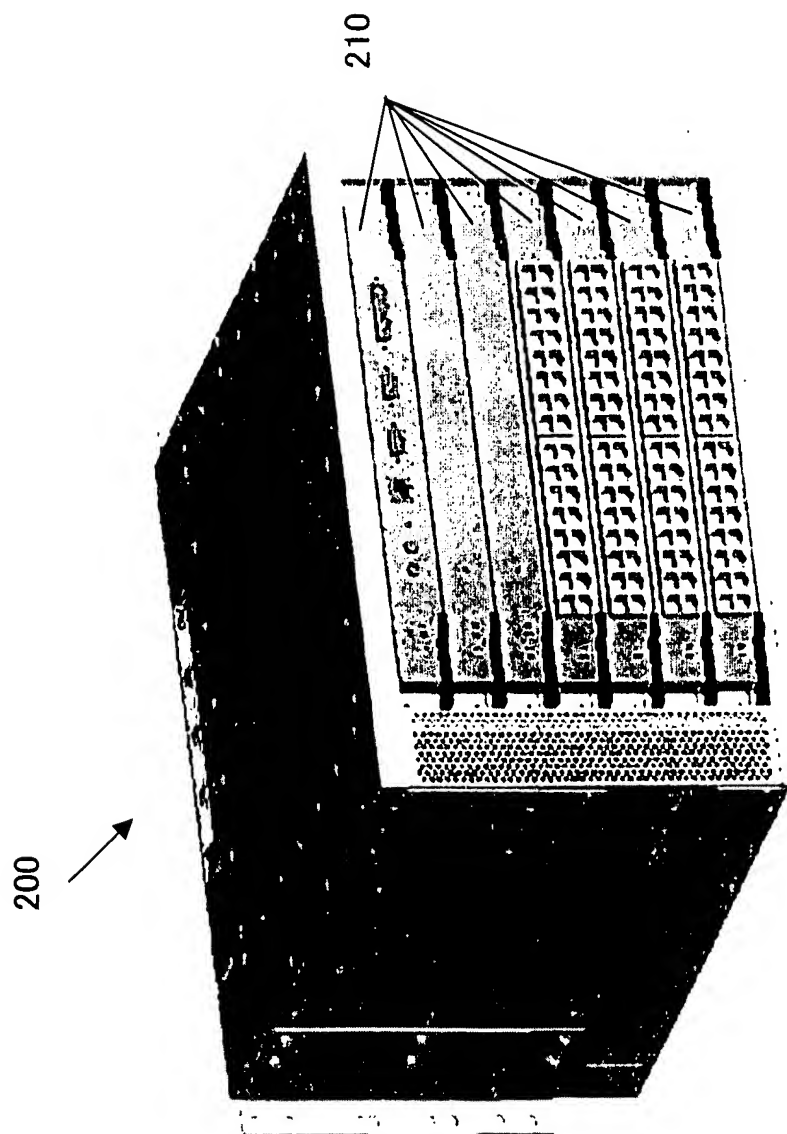


FIG. 11

FIG. 12B

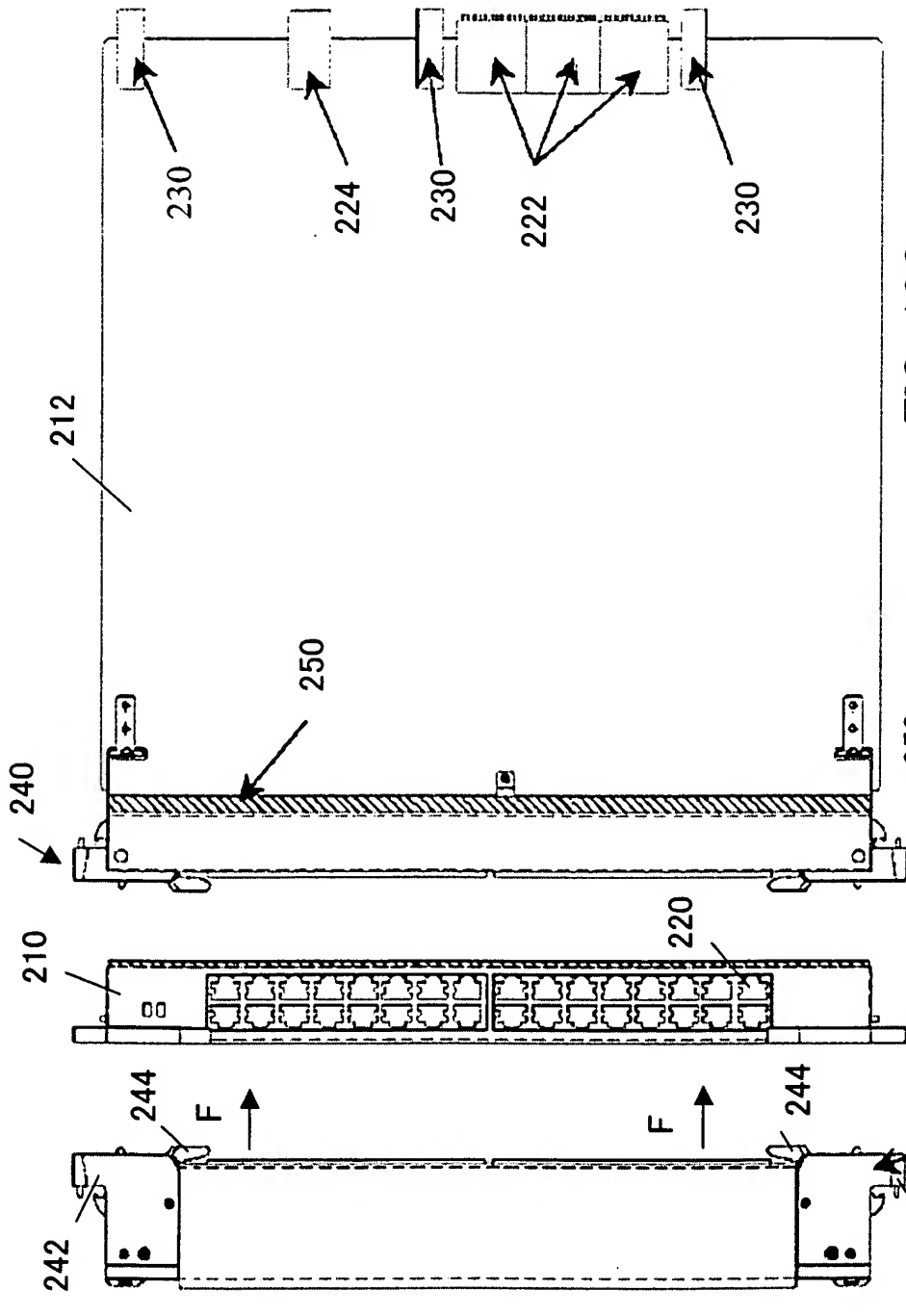


FIG. 12C

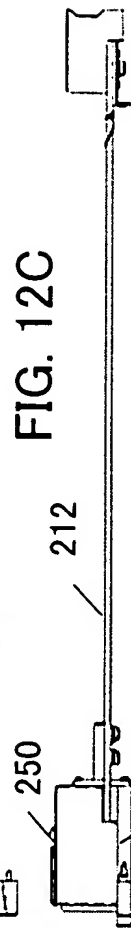


FIG. 12D



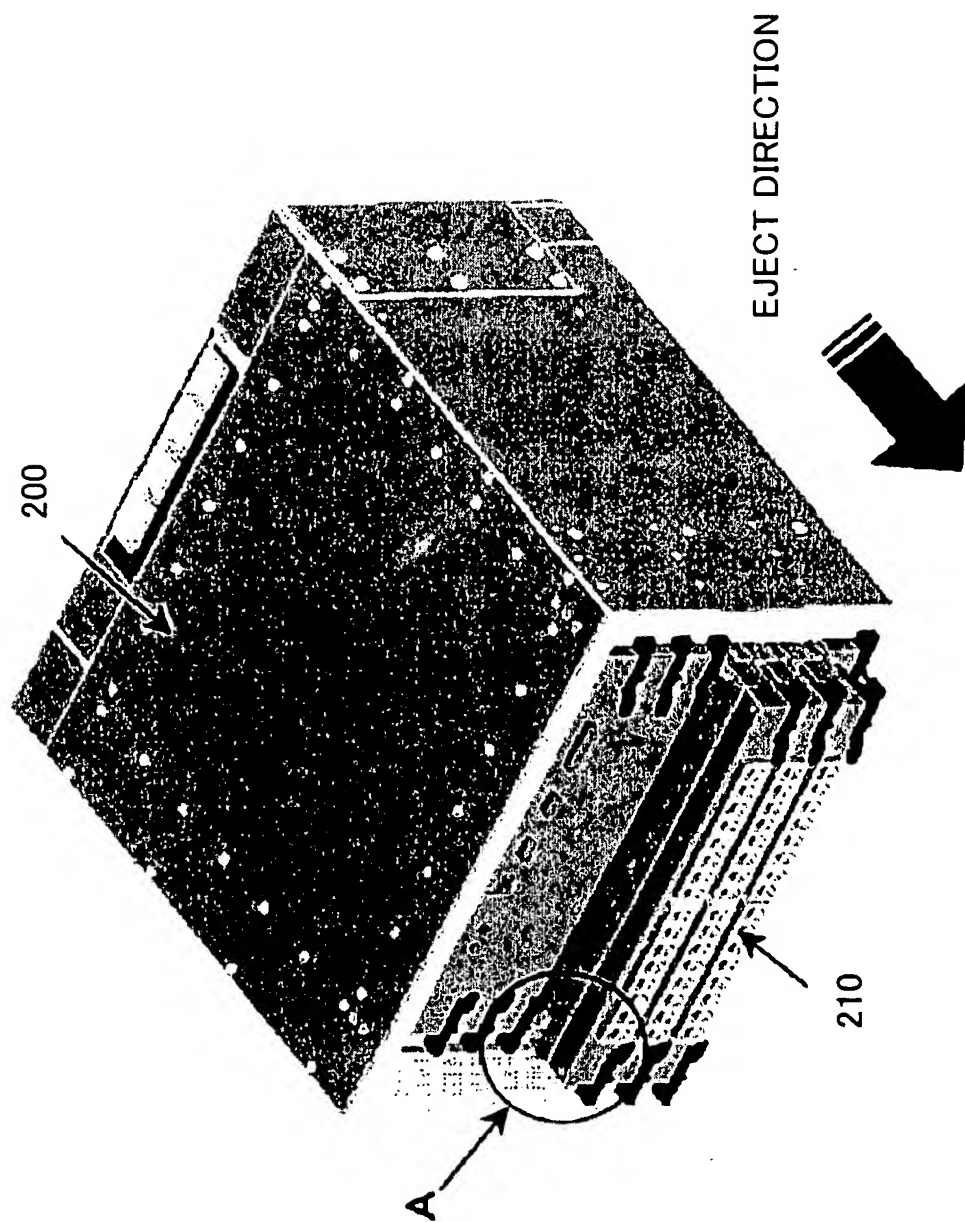


FIG. 13

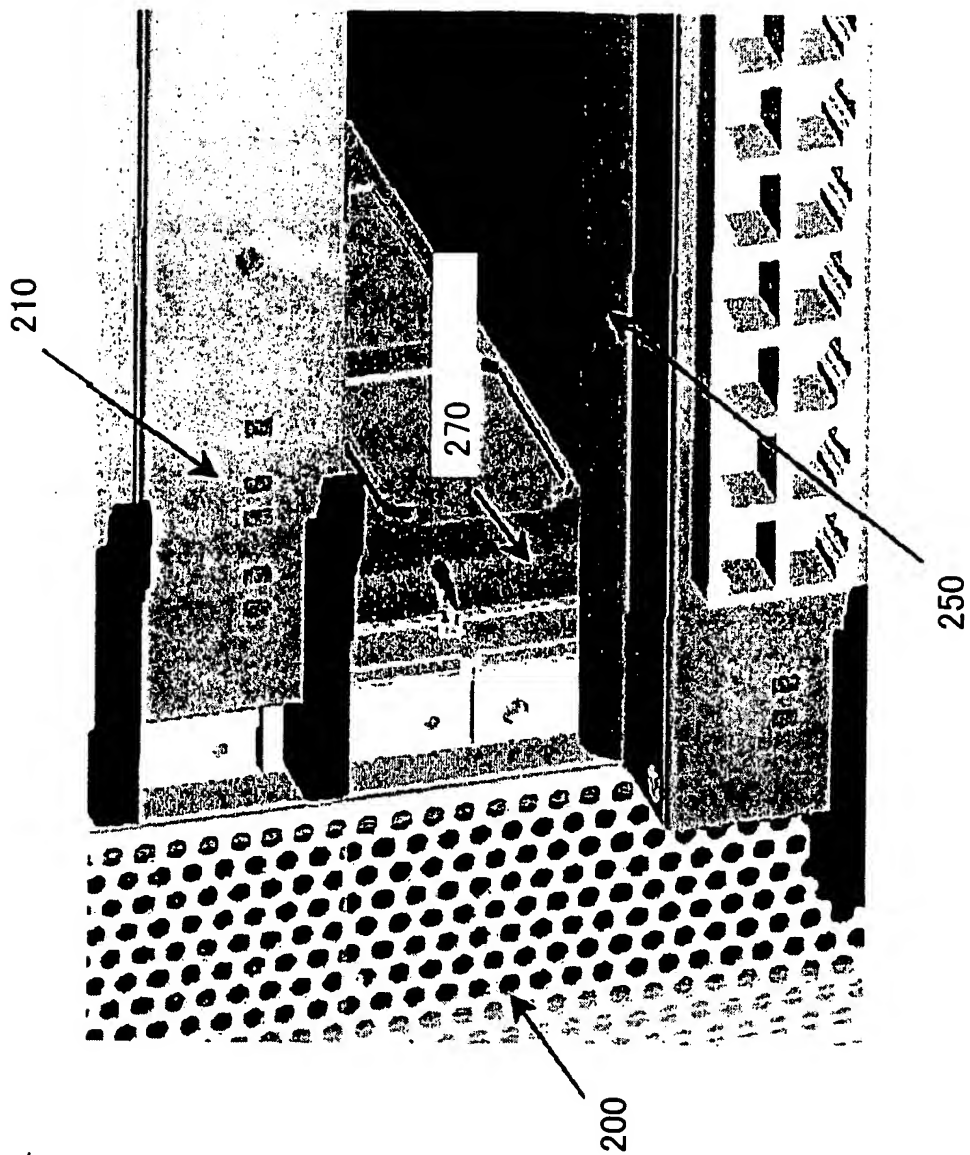


FIG. 14

FIG. 15A

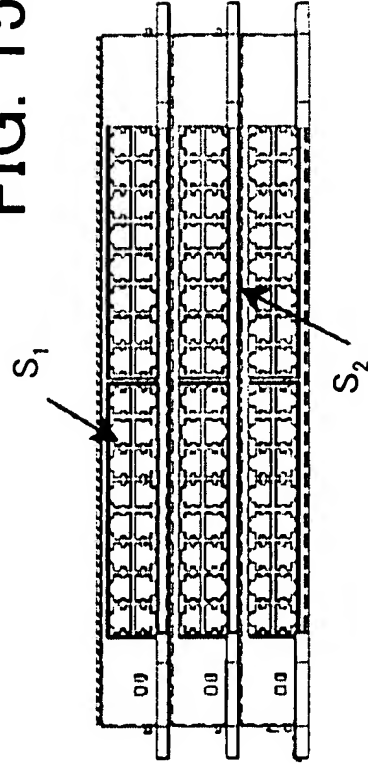


FIG. 15B

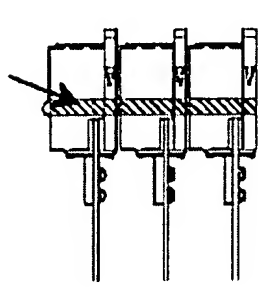
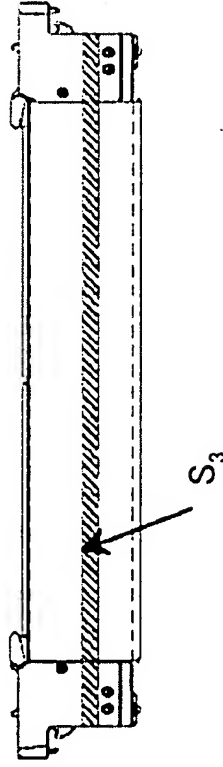


FIG. 15C



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FIG. 16

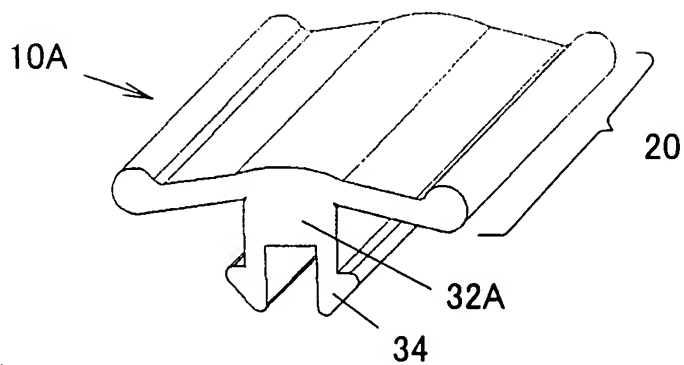


FIG. 17

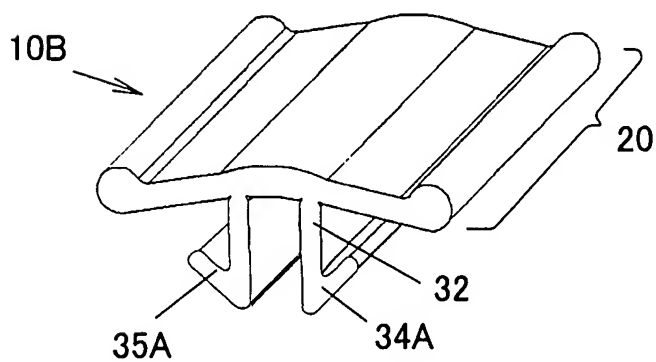
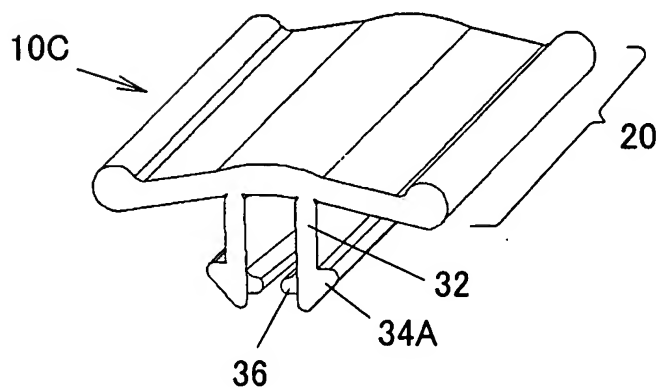


FIG. 18



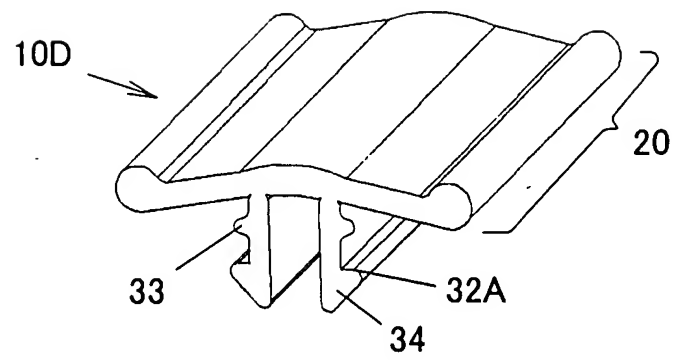


FIG. 19

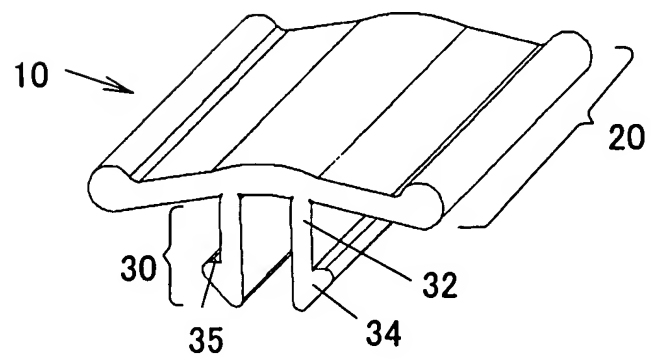


FIG. 20

PRIOR ART

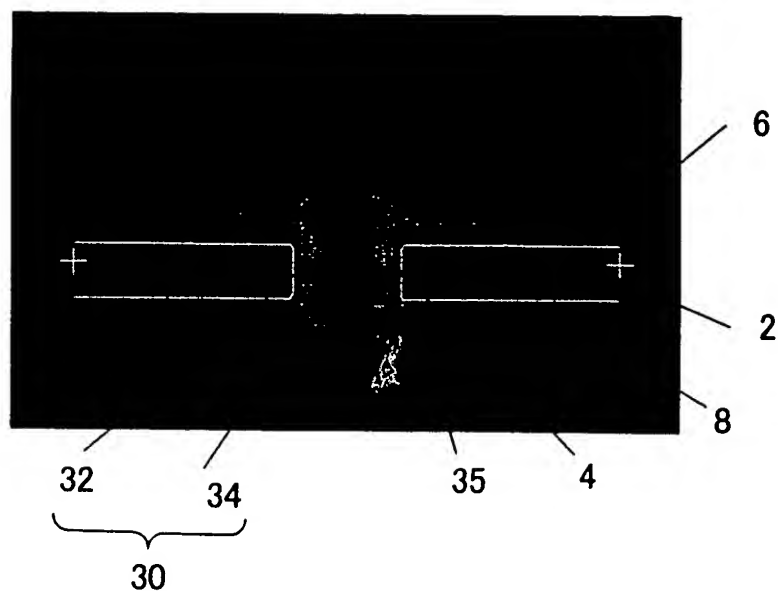


FIG. 21

PRIOR ART

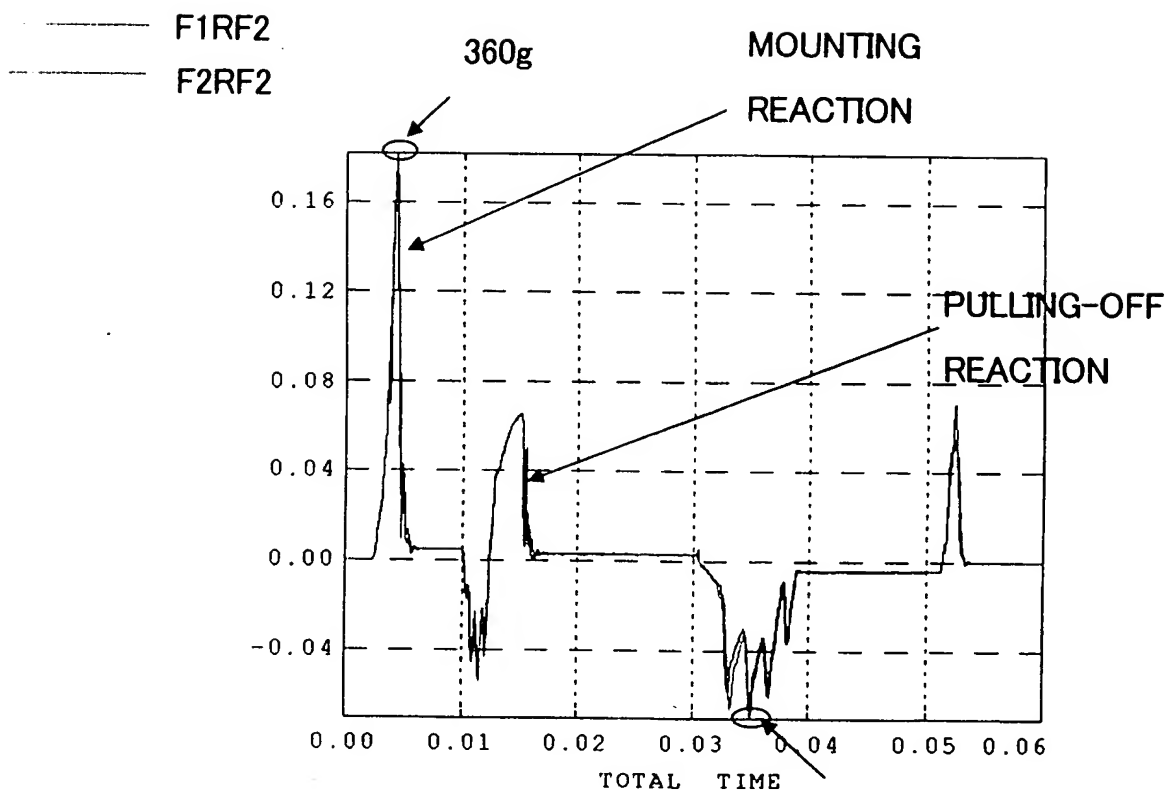


FIG. 22